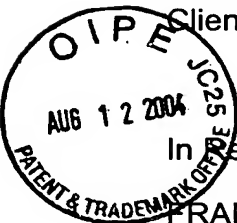


ITP

Docket Number: 081498-0306614

PATENT APPLICATION

Client Reference: GNC26US



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of

FRAMPTON E ELLIS

Group Art Unit:

Application No.: 10/684,657

Examiner:

Filed: October 15, 2003

Confirmation No.: 3226

For: GLOBAL NETWORK COMPUTERS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. As this application was filed after June 30, 2003, no copies of the cited U.S. patents are enclosed; however, one copy of each of the other cited references is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Applicants respectfully request the Examiner return an initialed copy of the enclosed Form PTO-1449 to Applicants with the next Office communication to indicate that the references has been considered, per MPEP § 609.

This Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

These references were cited in three Office Actions in three co-related U.S. applications, and in an International Search Report, dated February 6, 2004, in a corresponding foreign application. The German language reference has an English Abstract.

Respectfully Submitted,

A handwritten signature in cursive script that reads "Carlo M. Cotrone".

Carlo M. Cotrone

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**(Use as many sheets as necessary)**

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1

of

1

Attorney Docket Number

**081498-0306614**

**Complete if Known**

Application Number

10/684,657

Filing Date

10/15/2003

First Named Inventor

ELLIS E FRAMPTON

Art Unit

|               |
|---------------|
| Examiner Name |
|---------------|

Attorney Docket Number

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Cite No. <sup>1</sup> | Foreign Patent Document   | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages<br>or Relevant Figures Appear | T <sup>6</sup> |
|--------------------|-----------------------|---|--------------------------------|--|---|----------------|
|                    |                       | Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known) |                                |  |   |                |
|                    | A                     | DE 40 08 335 A1   | 09-26-1991                     | VIELHABEN  |   |                |
|                    |                       |   |                                |  |   |                |
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/08b (08-03)

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|   |   | Application Number       | 10/684,657       |                        |                |
|   |   | Filing Date              | 10/15/2003       |                        |                |
|   |   | First Named Inventor     | FRAMPTON E ELLIS |                        |                |
|   |   | Art Unit                 |                  |                        |                |
|   |   | Examiner Name            |                  |                        |                |
| Sheet   | 1 | of                       | 1                | Attorney Docket Number | 081498-0306614 |

| NON PATENT LITERATURE DOCUMENTS |                       |   |                |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials*              | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|                                 | A                     | NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, ( October 28, 1996).   |                |
|                                 | B                     | LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992).  |                |
|                                 | C                     | DICKINSON ET AL., "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, ( October 2-4, 1989).  |                |
|                                 | D                     | NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 ( No. 6), p. 1031-1040, ( June 6, 1995).  |                |
|                                 | E                     | ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, ( October 23-24, 1995).                 |                |
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| Examiner Signature |  | Date Considered |  |
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